



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

Results for M.Tech (R16/R13) I Semester Regular /Supplementary Examinations, JANUARY-2018 .

College: BABA INSTITUTE OF TECHNOLOGY AND SCIENCES:NR

Discrepancy pertaining to these results are to be submitted on or before 02-05-2018 with following documents at CE(PG) Office,JNTUK,Kakinada

Htno	Subcode	Subname	Internal	External	credits
14NR1D0403	G0401	INDUSTRIAL ROBOTICS	33	26	1
14NR1D0403	G0402	COMPUTER AIDED MANUFACTURING	37	28	1
14NR1D0403	G0403	SPECIAL MANUFACTURING PROCESSES	33	24	1
14NR1D0403	G0404	NANO TECHNOLOGY	33	27	1
14NR1D0403	G0407	COMPUTER AIDED PROCESS PLANNING	34	33	1
14NR1D0403	G0408	ADVANCED CAD LAB	25	43	1
14NR1D0403	G1508	GEOMETRIC MODELING	32	21	0
14NR1D0409	G1508	GEOMETRIC MODELING	34	18	0
14NR1D0413	G0401	INDUSTRIAL ROBOTICS	31	35	1
14NR1D0413	G0402	COMPUTER AIDED MANUFACTURING	34	16	0
14NR1D0413	G1508	GEOMETRIC MODELING	33	21	0
14NR1D5415	G4301	ELECTRICAL MACHINE MODELING & ANALYSIS	31	27	1
14NR1D5415	G4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	32	19	0
14NR1D5903	G4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	36	-1	0
14NR1D5903	G4002	ADVANCED GRAPH THEORY	35	-1	0
14NR1D5903	G4004	DATA MINING AND KNOWLEDGE DISCOVERY	36	-1	0
14NR1D5910	G4004	DATA MINING AND KNOWLEDGE DISCOVERY	36	-1	0
14NR1D5915	G4005	ADVANCED COMPUTER NETWORKS	31	27	1
14NR2D5910	G4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	37	-1	0
14NR2D8707	G2201	APPLIED MATHEMATICS	35	20	0
14NR2D8707	G8701	THEORY OF ELASTICITY	37	8	0
14NR2D8708	G2201	APPLIED MATHEMATICS	35	12	0
14NR2D8710	G8701	THEORY OF ELASTICITY	37	20	0
14NR2D8711	G8701	THEORY OF ELASTICITY	36	18	0
14NR2D8712	G8701	THEORY OF ELASTICITY	36	-1	0
15NR1D5405	G4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	31	25	1
15NR1D5405	G4303	ELECTRIC DRIVES-I	32	36	1
15NR1D5406	G4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	36	17	0
15NR1D5408	G4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	31	19	0
15NR1D5408	G4304	FLEXIBLE AC TRANSMISSION SYSTEMS	28	40	1
15NR1D5412	G4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	29	19	0
15NR1D5412	G4304	FLEXIBLE AC TRANSMISSION SYSTEMS	34	26	1
15NR1D5703	G6802	VLSI TECHNOLOGY AND DESIGN	36	-1	0
15NR1D5709	G6802	VLSI TECHNOLOGY AND DESIGN	32	19	0
15NR1D5712	G5701	DIGITAL DESIGN USING HDL	34	-1	0
15NR1D5712	G6802	VLSI TECHNOLOGY AND DESIGN	37	5	0
15NR1D5712	G6803	CMOS ANALOG IC DESIGN	33	6	0
15NR1D5712	G6809	CMOS DIGITAL IC DESIGN	37	2	0
15NR1D5713	G6802	VLSI TECHNOLOGY AND DESIGN	34	6	0
15NR1D5713	G6803	CMOS ANALOG IC DESIGN	31	28	1
15NR1D5713	G6804	CPLD AND FPGA ARCHITECTURES AND APPLICAT	34	7	0

Htno	Subcode	Subname	Internal	External	credits
15NR1D5713	G6806	DIGITAL SYSTEM DESIGN	30	0	0
15NR1D5713	G6809	CMOS DIGITAL IC DESIGN	36	7	0
15NR2D5408	G4301	ELECTRICAL MACHINE MODELING & ANALYSIS	29	10	0
15NR2D5408	G4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	31	15	0
15NR2D5408	G4303	ELECTRIC DRIVES-I	26	26	1
15NR2D5408	G4304	FLEXIBLE AC TRANSMISSION SYSTEMS	29	-1	0
15NR2D5408	G4305	POWER QUALITY	26	-1	0
15NR2D5408	G5602	HVDC TRANSMISSION	26	26	1
15NR2D5412	G4301	ELECTRICAL MACHINE MODELING & ANALYSIS	33	-1	0
15NR2D5412	G4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	29	8	0
15NR2D5412	G4303	ELECTRIC DRIVES-I	33	-1	0
15NR2D5412	G4304	FLEXIBLE AC TRANSMISSION SYSTEMS	33	6	0
15NR2D5412	G4305	POWER QUALITY	33	-1	0
15NR2D5412	G5602	HVDC TRANSMISSION	33	27	1
15NR2D5413	G4301	ELECTRICAL MACHINE MODELING & ANALYSIS	28	17	0
15NR2D5413	G4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	31	16	0
15NR2D5705	G6802	VLSI TECHNOLOGY AND DESIGN	33	8	0
15NR2D5705	G6803	CMOS ANALOG IC DESIGN	32	5	0
15NR2D5705	G6804	CPLD AND FPGA ARCHITECTURES AND APPLICAT	34	0	0
15NR2D5705	G6806	DIGITAL SYSTEM DESIGN	29	0	0
15NR2D5705	G6809	CMOS DIGITAL IC DESIGN	35	1	0
15NR2D5906	G4003	PARALLEL ALGORITHMS	38	38	1
15NR2D8702	G2201	APPLIED MATHEMATICS	34	16	0
15NR2D8702	G8701	THEORY OF ELASTICITY	34	6	0
15NR2D8705	G2201	APPLIED MATHEMATICS	36	28	1
15NR2D8705	G8701	THEORY OF ELASTICITY	37	18	0
15NR2D8705	G8703	STRUCTURAL DYNAMICS	36	-1	0
15NR2D8708	G2201	APPLIED MATHEMATICS	35	8	0
15NR2D8708	G8701	THEORY OF ELASTICITY	36	12	0
15NR2D8708	G8703	STRUCTURAL DYNAMICS	36	8	0
15NR2D8710	G2201	APPLIED MATHEMATICS	36	28	1
15NR2D8711	G2201	APPLIED MATHEMATICS	35	28	1
15NR2D8711	G8707	REPAIR AND REHABILITATION OF STRUCTURES	36	29	1
15NR2D8712	G8701	THEORY OF ELASTICITY	37	-1	0
15NR2D8712	G8702	MATRIX ANALYSIS OF STRUCTURES	36	-1	0
15NR2D8716	G2201	APPLIED MATHEMATICS	35	-1	0
15NR2D8716	G8701	THEORY OF ELASTICITY	37	-1	0
15NR2D8716	G8702	MATRIX ANALYSIS OF STRUCTURES	35	-1	0
15NR2D8716	G8703	STRUCTURAL DYNAMICS	37	-1	0
15NR2D8716	G8707	REPAIR AND REHABILITATION OF STRUCTURES	37	-1	0
16NR1D0401	I0401	INDUSTRIAL ROBOTICS	33	32	1
16NR1D0401	I0402	COMPUTER AIDED MANUFACTURING	36	17	0
16NR1D0401	I0403	SPECIAL MANUFACTURING PROCESSES	35	4	0
16NR1D0401	I1506	GEOMETRIC MODELLING ELECTIVE 1	30	6	0
16NR1D0401	I1809	NANO TECHNOLOGY ELECTIVE 2	33	3	0
16NR1D0404	I0402	COMPUTER AIDED MANUFACTURING	36	-1	0
16NR1D0404	I0403	SPECIAL MANUFACTURING PROCESSES	37	-1	0
16NR1D0404	I1506	GEOMETRIC MODELLING ELECTIVE 1	32	-1	0
16NR1D0404	I1809	NANO TECHNOLOGY ELECTIVE 2	35	-1	0
16NR1D0406	I0401	INDUSTRIAL ROBOTICS	34	41	1
16NR1D0406	I0402	COMPUTER AIDED MANUFACTURING	35	33	1

Htno	Subcode	Subname	Internal	External	credits
16NR1D0406	I0403	SPECIAL MANUFACTURING PROCESSES	36	43	1
16NR1D0406	I1506	GEOMETRIC MODELLING ELECTIVE 1	31	16	0
16NR1D5402	I5602	HVDC TRANSMISSION	38	31	1
16NR1D5403	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	32	28	1
16NR1D5403	I4305	POWER QUALITY	30	33	1
16NR1D5403	I5602	HVDC TRANSMISSION	34	42	1
16NR1D5404	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	29	17	0
16NR1D5404	I4303	POWER ELECTRONIC CONTROL OF DC DRIVES	33	24	1
16NR1D5404	I4304	FLEXIBLE AC TRANSMISSION SYSTEMS	35	9	0
16NR1D5404	I4309	SIMULATION LAB	36	30	1
16NR1D5405	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	33	24	1
16NR1D5406	I4301	ELECTRICAL MACHINE MODELING & ANALYSIS	34	45	1
16NR1D5406	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	35	15	0
16NR1D5406	I4303	POWER ELECTRONIC CONTROL OF DC DRIVES	36	26	1
16NR1D5406	I4304	FLEXIBLE AC TRANSMISSION SYSTEMS	35	16	0
16NR1D5406	I5602	HVDC TRANSMISSION	35	26	1
16NR1D5408	I4301	ELECTRICAL MACHINE MODELING & ANALYSIS	33	-1	0
16NR1D5408	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	31	-1	0
16NR1D5408	I4303	POWER ELECTRONIC CONTROL OF DC DRIVES	33	-1	0
16NR1D5408	I4309	SIMULATION LAB	30	-1	0
16NR1D5703	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	35	32	1
16NR1D5706	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	37	-1	0
16NR1D5706	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	37	-1	0
16NR1D5706	I6801	DIGITAL SYSTEM DESIGN	33	0	0
16NR1D5706	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	35	-1	0
16NR1D5706	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	38	-1	0
16NR1D5901	I2504	WEB TECHNOLOGIES	32	37	1
16NR1D5903	I2504	WEB TECHNOLOGIES	31	31	1
16NR1D5903	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	32	24	1
16NR1D5903	I4003	PARALLEL ALGORITHMS	31	2	0
16NR1D5909	I5902	CST LAB 1	32	54	1
16NR1D5910	I4004	DATA MINING AND KNOWLEDGE DISCOVERY	32	29	1
16NR1D5911	I2504	WEB TECHNOLOGIES	32	31	1
16NR2D5401	I4301	ELECTRICAL MACHINE MODELING & ANALYSIS	32	45	1
16NR2D5401	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	30	26	1
16NR2D5401	I4309	SIMULATION LAB	28	35	1
16NR2D5401	I5602	HVDC TRANSMISSION	32	29	1
16NR2D5403	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	35	30	1
16NR2D5403	I4305	POWER QUALITY	33	27	1
16NR2D5403	I5602	HVDC TRANSMISSION	35	34	1
16NR2D5702	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	18	-1	0
16NR2D5702	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	35	-1	0
16NR2D5702	I6801	DIGITAL SYSTEM DESIGN	30	-1	0
16NR2D5702	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	33	5	0
16NR2D5702	I6803	CMOS ANALOG IC DESIGN	32	24	1
16NR2D8703	I2201	ADVANCED MATHEMATICS	35	24	1
16NR2D8703	I8701	THEORY OF ELASTICITY	32	24	1
16NR2D8703	I8702	MATRIX ANALYSIS OF STRUCTURES	32	43	1
16NR2D8703	I8703	STRUCTURAL DYNAMICS	29	13	0
16NR2D8703	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	31	25	1
16NR2D8703	I8707	REPAIR AND REHABILITATION OF STRUCTURES	35	37	1

Htno	Subcode	Subname	Internal	External	credits
16NR2D8706	I2201	ADVANCED MATHEMATICS	34	24	1
16NR2D8706	I8701	THEORY OF ELASTICITY	34	12	0
16NR2D8706	I8702	MATRIX ANALYSIS OF STRUCTURES	36	30	1
16NR2D8706	I8703	STRUCTURAL DYNAMICS	33	8	0
16NR2D8706	I8707	REPAIR AND REHABILITATION OF STRUCTURES	36	34	1
16NR2D8707	I2201	ADVANCED MATHEMATICS	33	18	0
16NR2D8707	I8702	MATRIX ANALYSIS OF STRUCTURES	33	38	1
16NR2D8708	I8701	THEORY OF ELASTICITY	34	15	0
16NR2D8709	I2201	ADVANCED MATHEMATICS	36	-1	0
16NR2D8709	I8701	THEORY OF ELASTICITY	32	-1	0
16NR2D8709	I8702	MATRIX ANALYSIS OF STRUCTURES	38	30	1
16NR2D8709	I8703	STRUCTURAL DYNAMICS	29	6	0
16NR2D8709	I8707	REPAIR AND REHABILITATION OF STRUCTURES	36	30	1
16NR2D8711	I2201	ADVANCED MATHEMATICS	35	26	1
16NR2D8711	I8701	THEORY OF ELASTICITY	33	24	1
16NR2D8712	I2201	ADVANCED MATHEMATICS	18	7	0
16NR2D8712	I8701	THEORY OF ELASTICITY	16	26	0
16NR2D8712	I8702	MATRIX ANALYSIS OF STRUCTURES	16	-1	0
16NR2D8712	I8703	STRUCTURAL DYNAMICS	14	25	0
16NR2D8712	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	18	8	0
16NR2D8712	I8707	REPAIR AND REHABILITATION OF STRUCTURES	18	39	1
16NR2D8713	I2201	ADVANCED MATHEMATICS	33	11	0
17NR1D0401	I0401	INDUSTRIAL ROBOTICS	38	32	1
17NR1D0401	I0402	COMPUTER AIDED MANUFACTURING	36	32	1
17NR1D0401	I0403	SPECIAL MANUFACTURING PROCESSES	36	40	1
17NR1D0401	I0406	COMPUTER AIDED PROCESS PLANNING ELECTIV	37	26	1
17NR1D0401	I0407	ADVANCED CAD LAB	38	58	1
17NR1D0401	I1506	GEOMETRIC MODELLING ELECTIVE 1	36	18	0
17NR1D0401	I1809	NANO TECHNOLOGY ELECTIVE 2	36	27	1
17NR1D0402	I0401	INDUSTRIAL ROBOTICS	33	35	1
17NR1D0402	I0402	COMPUTER AIDED MANUFACTURING	32	24	1
17NR1D0402	I0403	SPECIAL MANUFACTURING PROCESSES	34	40	1
17NR1D0402	I0406	COMPUTER AIDED PROCESS PLANNING ELECTIV	33	24	1
17NR1D0402	I0407	ADVANCED CAD LAB	32	51	1
17NR1D0402	I1506	GEOMETRIC MODELLING ELECTIVE 1	33	6	0
17NR1D0402	I1809	NANO TECHNOLOGY ELECTIVE 2	35	17	0
17NR1D0403	I0401	INDUSTRIAL ROBOTICS	34	28	1
17NR1D0403	I0402	COMPUTER AIDED MANUFACTURING	33	24	1
17NR1D0403	I0403	SPECIAL MANUFACTURING PROCESSES	35	30	1
17NR1D0403	I0406	COMPUTER AIDED PROCESS PLANNING ELECTIV	35	24	1
17NR1D0403	I0407	ADVANCED CAD LAB	34	51	1
17NR1D0403	I1506	GEOMETRIC MODELLING ELECTIVE 1	34	0	0
17NR1D0403	I1809	NANO TECHNOLOGY ELECTIVE 2	35	29	1
17NR1D0404	I0401	INDUSTRIAL ROBOTICS	33	28	1
17NR1D0404	I0402	COMPUTER AIDED MANUFACTURING	32	31	1
17NR1D0404	I0403	SPECIAL MANUFACTURING PROCESSES	35	40	1
17NR1D0404	I0406	COMPUTER AIDED PROCESS PLANNING ELECTIV	32	27	1
17NR1D0404	I0407	ADVANCED CAD LAB	34	49	1
17NR1D0404	I1506	GEOMETRIC MODELLING ELECTIVE 1	34	0	0
17NR1D0404	I1809	NANO TECHNOLOGY ELECTIVE 2	34	31	1
17NR1D0405	I0401	INDUSTRIAL ROBOTICS	39	48	1

Htno	Subcode	Subname	Internal	External	credits
17NR1D0405	I0402	COMPUTER AIDED MANUFACTURING	35	27	1
17NR1D0405	I0403	SPECIAL MANUFACTURING PROCESSES	37	-1	0
17NR1D0405	I0406	COMPUTER AIDED PROCESS PLANNING ELECTIV	37	27	1
17NR1D0405	I0407	ADVANCED CAD LAB	39	58	1
17NR1D0405	I1506	GEOMETRIC MODELLING ELECTIVE 1	35	17	0
17NR1D0405	I1809	NANO TECHNOLOGY ELECTIVE 2	37	31	1
17NR1D0406	I0401	INDUSTRIAL ROBOTICS	34	41	1
17NR1D0406	I0402	COMPUTER AIDED MANUFACTURING	31	42	1
17NR1D0406	I0403	SPECIAL MANUFACTURING PROCESSES	34	47	1
17NR1D0406	I0406	COMPUTER AIDED PROCESS PLANNING ELECTIV	31	31	1
17NR1D0406	I0407	ADVANCED CAD LAB	33	50	1
17NR1D0406	I1506	GEOMETRIC MODELLING ELECTIVE 1	32	30	1
17NR1D0406	I1809	NANO TECHNOLOGY ELECTIVE 2	32	44	1
17NR1D0407	I0401	INDUSTRIAL ROBOTICS	33	31	1
17NR1D0407	I0402	COMPUTER AIDED MANUFACTURING	31	27	1
17NR1D0407	I0403	SPECIAL MANUFACTURING PROCESSES	33	41	1
17NR1D0407	I0406	COMPUTER AIDED PROCESS PLANNING ELECTIV	34	26	1
17NR1D0407	I0407	ADVANCED CAD LAB	35	51	1
17NR1D0407	I1506	GEOMETRIC MODELLING ELECTIVE 1	34	32	1
17NR1D0407	I1809	NANO TECHNOLOGY ELECTIVE 2	33	16	0
17NR1D0408	I0401	INDUSTRIAL ROBOTICS	33	35	1
17NR1D0408	I0402	COMPUTER AIDED MANUFACTURING	33	33	1
17NR1D0408	I0403	SPECIAL MANUFACTURING PROCESSES	34	47	1
17NR1D0408	I0406	COMPUTER AIDED PROCESS PLANNING ELECTIV	31	30	1
17NR1D0408	I0407	ADVANCED CAD LAB	32	49	1
17NR1D0408	I1506	GEOMETRIC MODELLING ELECTIVE 1	32	28	1
17NR1D0408	I1809	NANO TECHNOLOGY ELECTIVE 2	33	28	1
17NR1D0409	I0401	INDUSTRIAL ROBOTICS	33	28	1
17NR1D0409	I0402	COMPUTER AIDED MANUFACTURING	32	35	1
17NR1D0409	I0403	SPECIAL MANUFACTURING PROCESSES	34	49	1
17NR1D0409	I0406	COMPUTER AIDED PROCESS PLANNING ELECTIV	33	30	1
17NR1D0409	I0407	ADVANCED CAD LAB	35	47	1
17NR1D0409	I1506	GEOMETRIC MODELLING ELECTIVE 1	34	31	1
17NR1D0409	I1809	NANO TECHNOLOGY ELECTIVE 2	33	37	1
17NR1D0410	I0401	INDUSTRIAL ROBOTICS	34	10	0
17NR1D0410	I0402	COMPUTER AIDED MANUFACTURING	33	37	1
17NR1D0410	I0403	SPECIAL MANUFACTURING PROCESSES	36	29	1
17NR1D0410	I0406	COMPUTER AIDED PROCESS PLANNING ELECTIV	33	24	1
17NR1D0410	I0407	ADVANCED CAD LAB	36	51	1
17NR1D0410	I1506	GEOMETRIC MODELLING ELECTIVE 1	35	29	1
17NR1D0410	I1809	NANO TECHNOLOGY ELECTIVE 2	33	32	1
17NR1D0411	I0401	INDUSTRIAL ROBOTICS	31	-1	0
17NR1D0411	I0402	COMPUTER AIDED MANUFACTURING	32	-1	0
17NR1D0411	I0403	SPECIAL MANUFACTURING PROCESSES	33	-1	0
17NR1D0411	I0406	COMPUTER AIDED PROCESS PLANNING ELECTIV	30	-1	0
17NR1D0411	I0407	ADVANCED CAD LAB	0	-1	0
17NR1D0411	I1506	GEOMETRIC MODELLING ELECTIVE 1	34	-1	0
17NR1D0411	I1809	NANO TECHNOLOGY ELECTIVE 2	30	-1	0
17NR1D0412	I0401	INDUSTRIAL ROBOTICS	33	39	1
17NR1D0412	I0402	COMPUTER AIDED MANUFACTURING	32	47	1
17NR1D0412	I0403	SPECIAL MANUFACTURING PROCESSES	36	52	1

Htno	Subcode	Subname	Internal	External	credits
17NR1D0412	I0406	COMPUTER AIDED PROCESS PLANNING ELECTIV	34	33	1
17NR1D0412	I0407	ADVANCED CAD LAB	32	45	1
17NR1D0412	I1506	GEOMETRIC MODELLING ELECTIVE 1	33	38	1
17NR1D0412	I1809	NANO TECHNOLOGY ELECTIVE 2	33	46	1
17NR1D0413	I0401	INDUSTRIAL ROBOTICS	32	28	1
17NR1D0413	I0402	COMPUTER AIDED MANUFACTURING	33	34	1
17NR1D0413	I0403	SPECIAL MANUFACTURING PROCESSES	34	50	1
17NR1D0413	I0406	COMPUTER AIDED PROCESS PLANNING ELECTIV	33	24	1
17NR1D0413	I0407	ADVANCED CAD LAB	35	50	1
17NR1D0413	I1506	GEOMETRIC MODELLING ELECTIVE 1	34	24	1
17NR1D0413	I1809	NANO TECHNOLOGY ELECTIVE 2	33	27	1
17NR1D0414	I0401	INDUSTRIAL ROBOTICS	14	-1	0
17NR1D0414	I0402	COMPUTER AIDED MANUFACTURING	11	-1	0
17NR1D0414	I0403	SPECIAL MANUFACTURING PROCESSES	14	-1	0
17NR1D0414	I0406	COMPUTER AIDED PROCESS PLANNING ELECTIV	15	-1	0
17NR1D0414	I0407	ADVANCED CAD LAB	0	-1	0
17NR1D0414	I1506	GEOMETRIC MODELLING ELECTIVE 1	14	-1	0
17NR1D0414	I1809	NANO TECHNOLOGY ELECTIVE 2	14	-1	0
17NR1D0415	I0401	INDUSTRIAL ROBOTICS	33	27	1
17NR1D0415	I0402	COMPUTER AIDED MANUFACTURING	32	31	1
17NR1D0415	I0403	SPECIAL MANUFACTURING PROCESSES	34	44	1
17NR1D0415	I0406	COMPUTER AIDED PROCESS PLANNING ELECTIV	32	26	1
17NR1D0415	I0407	ADVANCED CAD LAB	32	49	1
17NR1D0415	I1506	GEOMETRIC MODELLING ELECTIVE 1	34	17	0
17NR1D0415	I1809	NANO TECHNOLOGY ELECTIVE 2	32	13	0
17NR1D0416	I0401	INDUSTRIAL ROBOTICS	38	35	1
17NR1D0416	I0402	COMPUTER AIDED MANUFACTURING	35	43	1
17NR1D0416	I0403	SPECIAL MANUFACTURING PROCESSES	37	49	1
17NR1D0416	I0406	COMPUTER AIDED PROCESS PLANNING ELECTIV	37	28	1
17NR1D0416	I0407	ADVANCED CAD LAB	40	58	1
17NR1D0416	I1506	GEOMETRIC MODELLING ELECTIVE 1	37	30	1
17NR1D0416	I1809	NANO TECHNOLOGY ELECTIVE 2	34	31	1
17NR1D5401	I4301	ELECTRICAL MACHINE MODELING & ANALYSIS	37	39	1
17NR1D5401	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	38	31	1
17NR1D5401	I4303	POWER ELECTRONIC CONTROL OF DC DRIVES	32	35	1
17NR1D5401	I4304	FLEXIBLE AC TRANSMISSION SYSTEMS	35	36	1
17NR1D5401	I4305	POWER QUALITY	35	38	1
17NR1D5401	I4309	SIMULATION LAB	35	51	1
17NR1D5401	I5602	HVDC TRANSMISSION	33	42	1
17NR1D5402	I4301	ELECTRICAL MACHINE MODELING & ANALYSIS	36	48	1
17NR1D5402	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	36	36	1
17NR1D5402	I4303	POWER ELECTRONIC CONTROL OF DC DRIVES	31	36	1
17NR1D5402	I4304	FLEXIBLE AC TRANSMISSION SYSTEMS	32	35	1
17NR1D5402	I4305	POWER QUALITY	35	32	1
17NR1D5402	I4309	SIMULATION LAB	32	40	1
17NR1D5402	I5602	HVDC TRANSMISSION	33	30	1
17NR1D5403	I4301	ELECTRICAL MACHINE MODELING & ANALYSIS	37	52	1
17NR1D5403	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	34	37	1
17NR1D5403	I4303	POWER ELECTRONIC CONTROL OF DC DRIVES	28	28	1
17NR1D5403	I4304	FLEXIBLE AC TRANSMISSION SYSTEMS	29	28	1
17NR1D5403	I4305	POWER QUALITY	30	26	1

Htno	Subcode	Subname	Internal	External	credits
17NR1D5403	I4309	SIMULATION LAB	34	41	1
17NR1D5403	I5602	HVDC TRANSMISSION	30	30	1
17NR1D5404	I4301	ELECTRICAL MACHINE MODELING & ANALYSIS	37	54	1
17NR1D5404	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	36	26	1
17NR1D5404	I4303	POWER ELECTRONIC CONTROL OF DC DRIVES	32	27	1
17NR1D5404	I4304	FLEXIBLE AC TRANSMISSION SYSTEMS	32	40	1
17NR1D5404	I4305	POWER QUALITY	32	30	1
17NR1D5404	I4309	SIMULATION LAB	32	44	1
17NR1D5404	I5602	HVDC TRANSMISSION	32	42	1
17NR1D5405	I4301	ELECTRICAL MACHINE MODELING & ANALYSIS	36	15	0
17NR1D5405	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	34	10	0
17NR1D5405	I4303	POWER ELECTRONIC CONTROL OF DC DRIVES	32	15	0
17NR1D5405	I4304	FLEXIBLE AC TRANSMISSION SYSTEMS	35	19	0
17NR1D5405	I4305	POWER QUALITY	34	13	0
17NR1D5405	I4309	SIMULATION LAB	38	47	1
17NR1D5405	I5602	HVDC TRANSMISSION	34	12	0
17NR1D5406	I4301	ELECTRICAL MACHINE MODELING & ANALYSIS	39	46	1
17NR1D5406	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	38	24	1
17NR1D5406	I4303	POWER ELECTRONIC CONTROL OF DC DRIVES	32	30	1
17NR1D5406	I4304	FLEXIBLE AC TRANSMISSION SYSTEMS	39	25	1
17NR1D5406	I4305	POWER QUALITY	38	26	1
17NR1D5406	I4309	SIMULATION LAB	35	45	1
17NR1D5406	I5602	HVDC TRANSMISSION	36	34	1
17NR1D5408	I4301	ELECTRICAL MACHINE MODELING & ANALYSIS	15	-1	0
17NR1D5408	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	17	-1	0
17NR1D5408	I4303	POWER ELECTRONIC CONTROL OF DC DRIVES	13	-1	0
17NR1D5408	I4304	FLEXIBLE AC TRANSMISSION SYSTEMS	16	-1	0
17NR1D5408	I4305	POWER QUALITY	16	-1	0
17NR1D5408	I4309	SIMULATION LAB	28	-1	0
17NR1D5408	I5602	HVDC TRANSMISSION	14	-1	0
17NR1D5409	I4301	ELECTRICAL MACHINE MODELING & ANALYSIS	36	39	1
17NR1D5409	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	36	17	0
17NR1D5409	I4303	POWER ELECTRONIC CONTROL OF DC DRIVES	30	24	1
17NR1D5409	I4304	FLEXIBLE AC TRANSMISSION SYSTEMS	32	32	1
17NR1D5409	I4305	POWER QUALITY	32	24	1
17NR1D5409	I4309	SIMULATION LAB	36	42	1
17NR1D5409	I5602	HVDC TRANSMISSION	32	35	1
17NR1D5410	I4301	ELECTRICAL MACHINE MODELING & ANALYSIS	35	46	1
17NR1D5410	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	35	31	1
17NR1D5410	I4303	POWER ELECTRONIC CONTROL OF DC DRIVES	30	29	1
17NR1D5410	I4304	FLEXIBLE AC TRANSMISSION SYSTEMS	33	32	1
17NR1D5410	I4305	POWER QUALITY	34	26	1
17NR1D5410	I4309	SIMULATION LAB	35	48	1
17NR1D5410	I5602	HVDC TRANSMISSION	33	35	1
17NR1D5411	I4301	ELECTRICAL MACHINE MODELING & ANALYSIS	39	47	1
17NR1D5411	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	36	24	1
17NR1D5411	I4303	POWER ELECTRONIC CONTROL OF DC DRIVES	33	35	1
17NR1D5411	I4304	FLEXIBLE AC TRANSMISSION SYSTEMS	37	35	1
17NR1D5411	I4305	POWER QUALITY	36	39	1
17NR1D5411	I4309	SIMULATION LAB	37	44	1
17NR1D5411	I5602	HVDC TRANSMISSION	36	40	1

Htno	Subcode	Subname	Internal	External	credits
17NR1D5701	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	35	27	1
17NR1D5701	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	36	26	1
17NR1D5701	I5704	FRONT END VLSI DESIGN LABORATORY	37	53	1
17NR1D5701	I6801	DIGITAL SYSTEM DESIGN	35	10	0
17NR1D5701	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	34	39	1
17NR1D5701	I6803	CMOS ANALOG IC DESIGN	36	34	1
17NR1D5701	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	31	30	1
17NR1D5702	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	36	34	1
17NR1D5702	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	33	34	1
17NR1D5702	I5704	FRONT END VLSI DESIGN LABORATORY	38	56	1
17NR1D5702	I6801	DIGITAL SYSTEM DESIGN	39	15	0
17NR1D5702	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	38	34	1
17NR1D5702	I6803	CMOS ANALOG IC DESIGN	37	47	1
17NR1D5702	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	35	34	1
17NR1D5703	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	36	36	1
17NR1D5703	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	36	32	1
17NR1D5703	I5704	FRONT END VLSI DESIGN LABORATORY	39	52	1
17NR1D5703	I6801	DIGITAL SYSTEM DESIGN	39	6	0
17NR1D5703	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	36	36	1
17NR1D5703	I6803	CMOS ANALOG IC DESIGN	36	40	1
17NR1D5703	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	37	19	0
17NR1D5704	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	37	31	1
17NR1D5704	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	36	37	1
17NR1D5704	I5704	FRONT END VLSI DESIGN LABORATORY	38	56	1
17NR1D5704	I6801	DIGITAL SYSTEM DESIGN	37	12	0
17NR1D5704	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	35	33	1
17NR1D5704	I6803	CMOS ANALOG IC DESIGN	35	28	1
17NR1D5704	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	33	30	1
17NR1D5705	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	34	1	0
17NR1D5705	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	36	30	1
17NR1D5705	I5704	FRONT END VLSI DESIGN LABORATORY	35	52	1
17NR1D5705	I6801	DIGITAL SYSTEM DESIGN	37	0	0
17NR1D5705	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	32	15	0
17NR1D5705	I6803	CMOS ANALOG IC DESIGN	34	17	0
17NR1D5705	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	33	26	1
17NR1D5706	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	36	33	1
17NR1D5706	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	36	38	1
17NR1D5706	I5704	FRONT END VLSI DESIGN LABORATORY	36	53	1
17NR1D5706	I6801	DIGITAL SYSTEM DESIGN	39	13	0
17NR1D5706	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	38	29	1
17NR1D5706	I6803	CMOS ANALOG IC DESIGN	36	32	1
17NR1D5706	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	30	38	1
17NR1D5707	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	38	24	1
17NR1D5707	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	39	29	1
17NR1D5707	I5704	FRONT END VLSI DESIGN LABORATORY	39	56	1
17NR1D5707	I6801	DIGITAL SYSTEM DESIGN	39	18	0
17NR1D5707	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	38	33	1
17NR1D5707	I6803	CMOS ANALOG IC DESIGN	36	27	1
17NR1D5707	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	36	36	1
17NR1D5708	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	30	17	0
17NR1D5708	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	36	25	1

Htno	Subcode	Subname	Internal	External	credits
17NR1D5708	I5704	FRONT END VLSI DESIGN LABORATORY	35	53	1
17NR1D5708	I6801	DIGITAL SYSTEM DESIGN	38	10	0
17NR1D5708	I6802	VLSI TECHNOLOGY AND DESIGN ELECTIVE 1	35	26	1
17NR1D5708	I6803	CMOS ANALOG IC DESIGN	38	9	0
17NR1D5708	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	33	32	1
17NR1D5901	I2504	WEB TECHNOLOGIES	36	34	1
17NR1D5901	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	35	42	1
17NR1D5901	I4003	PARALLEL ALGORITHMS	36	37	1
17NR1D5901	I4004	DATA MINING AND KNOWLEDGE DISCOVERY	35	24	1
17NR1D5901	I5901	DISTRIBUTED SYSTEMS	37	38	1
17NR1D5901	I5902	CST LAB 1	36	54	1
17NR1D5901	I8401	CYBER SECURITY	35	34	1
17NR1D5902	I2504	WEB TECHNOLOGIES	36	44	1
17NR1D5902	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	36	41	1
17NR1D5902	I4003	PARALLEL ALGORITHMS	35	42	1
17NR1D5902	I4004	DATA MINING AND KNOWLEDGE DISCOVERY	37	30	1
17NR1D5902	I5901	DISTRIBUTED SYSTEMS	36	41	1
17NR1D5902	I5902	CST LAB 1	35	54	1
17NR1D5902	I8401	CYBER SECURITY	36	32	1
17NR1D5903	I2504	WEB TECHNOLOGIES	36	37	1
17NR1D5903	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	35	44	1
17NR1D5903	I4003	PARALLEL ALGORITHMS	37	47	1
17NR1D5903	I4004	DATA MINING AND KNOWLEDGE DISCOVERY	37	36	1
17NR1D5903	I5901	DISTRIBUTED SYSTEMS	36	42	1
17NR1D5903	I5902	CST LAB 1	38	58	1
17NR1D5903	I8401	CYBER SECURITY	35	34	1
17NR1D5904	I2504	WEB TECHNOLOGIES	34	38	1
17NR1D5904	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	35	43	1
17NR1D5904	I4003	PARALLEL ALGORITHMS	36	39	1
17NR1D5904	I4004	DATA MINING AND KNOWLEDGE DISCOVERY	37	32	1
17NR1D5904	I5901	DISTRIBUTED SYSTEMS	36	46	1
17NR1D5904	I5902	CST LAB 1	39	58	1
17NR1D5904	I8401	CYBER SECURITY	37	32	1
17NR1D5905	I2504	WEB TECHNOLOGIES	36	31	1
17NR1D5905	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	36	38	1
17NR1D5905	I4003	PARALLEL ALGORITHMS	35	29	1
17NR1D5905	I4004	DATA MINING AND KNOWLEDGE DISCOVERY	36	26	1
17NR1D5905	I5901	DISTRIBUTED SYSTEMS	34	35	1
17NR1D5905	I5902	CST LAB 1	35	54	1
17NR1D5905	I8401	CYBER SECURITY	37	24	1
17NR1D5906	I2504	WEB TECHNOLOGIES	37	36	1
17NR1D5906	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	34	40	1
17NR1D5906	I4003	PARALLEL ALGORITHMS	34	42	1
17NR1D5906	I4004	DATA MINING AND KNOWLEDGE DISCOVERY	36	30	1
17NR1D5906	I5901	DISTRIBUTED SYSTEMS	37	42	1
17NR1D5906	I5902	CST LAB 1	38	55	1
17NR1D5906	I8401	CYBER SECURITY	35	33	1
17NR1D5907	I2504	WEB TECHNOLOGIES	35	39	1
17NR1D5907	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	37	41	1
17NR1D5907	I4003	PARALLEL ALGORITHMS	35	38	1
17NR1D5907	I4004	DATA MINING AND KNOWLEDGE DISCOVERY	36	35	1

Htno	Subcode	Subname	Internal	External	credits
17NR1D5907	I5901	DISTRIBUTED SYSTEMS	35	42	1
17NR1D5907	I5902	CST LAB 1	38	56	1
17NR1D5907	I8401	CYBER SECURITY	37	34	1
17NR1D5908	I2504	WEB TECHNOLOGIES	34	41	1
17NR1D5908	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	36	33	1
17NR1D5908	I4003	PARALLEL ALGORITHMS	36	35	1
17NR1D5908	I4004	DATA MINING AND KNOWLEDGE DISCOVERY	36	27	1
17NR1D5908	I5901	DISTRIBUTED SYSTEMS	35	14	0
17NR1D5908	I5902	CST LAB 1	35	54	1
17NR1D5908	I8401	CYBER SECURITY	36	24	1
17NR1D5909	I2504	WEB TECHNOLOGIES	36	43	1
17NR1D5909	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	37	48	1
17NR1D5909	I4003	PARALLEL ALGORITHMS	37	49	1
17NR1D5909	I4004	DATA MINING AND KNOWLEDGE DISCOVERY	36	37	1
17NR1D5909	I5901	DISTRIBUTED SYSTEMS	36	46	1
17NR1D5909	I5902	CST LAB 1	38	55	1
17NR1D5909	I8401	CYBER SECURITY	37	36	1
17NR2D5401	I4301	ELECTRICAL MACHINE MODELING & ANALYSIS	38	54	1
17NR2D5401	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	37	38	1
17NR2D5401	I4303	POWER ELECTRONIC CONTROL OF DC DRIVES	33	46	1
17NR2D5401	I4304	FLEXIBLE AC TRANSMISSION SYSTEMS	36	45	1
17NR2D5401	I4305	POWER QUALITY	34	40	1
17NR2D5401	I4309	SIMULATION LAB	38	35	1
17NR2D5401	I5602	HVDC TRANSMISSION	34	42	1
17NR2D5402	I4301	ELECTRICAL MACHINE MODELING & ANALYSIS	37	39	1
17NR2D5402	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	35	34	1
17NR2D5402	I4303	POWER ELECTRONIC CONTROL OF DC DRIVES	31	33	1
17NR2D5402	I4304	FLEXIBLE AC TRANSMISSION SYSTEMS	34	26	1
17NR2D5402	I4305	POWER QUALITY	34	29	1
17NR2D5402	I4309	SIMULATION LAB	36	43	1
17NR2D5402	I5602	HVDC TRANSMISSION	35	30	1
17NR2D5403	I4301	ELECTRICAL MACHINE MODELING & ANALYSIS	32	40	1
17NR2D5403	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	34	26	1
17NR2D5403	I4303	POWER ELECTRONIC CONTROL OF DC DRIVES	30	29	1
17NR2D5403	I4304	FLEXIBLE AC TRANSMISSION SYSTEMS	31	24	1
17NR2D5403	I4305	POWER QUALITY	33	29	1
17NR2D5403	I4309	SIMULATION LAB	38	50	1
17NR2D5403	I5602	HVDC TRANSMISSION	32	36	1
17NR2D5404	I4301	ELECTRICAL MACHINE MODELING & ANALYSIS	37	35	1
17NR2D5404	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	35	26	1
17NR2D5404	I4303	POWER ELECTRONIC CONTROL OF DC DRIVES	32	25	1
17NR2D5404	I4304	FLEXIBLE AC TRANSMISSION SYSTEMS	33	29	1
17NR2D5404	I4305	POWER QUALITY	35	25	1
17NR2D5404	I4309	SIMULATION LAB	36	46	1
17NR2D5404	I5602	HVDC TRANSMISSION	34	42	1
17NR2D5405	I4301	ELECTRICAL MACHINE MODELING & ANALYSIS	29	37	1
17NR2D5405	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	29	24	1
17NR2D5405	I4303	POWER ELECTRONIC CONTROL OF DC DRIVES	27	37	1
17NR2D5405	I4304	FLEXIBLE AC TRANSMISSION SYSTEMS	27	26	1
17NR2D5405	I4305	POWER QUALITY	31	26	1
17NR2D5405	I4309	SIMULATION LAB	30	30	1

Htno	Subcode	Subname	Internal	External	credits
17NR2D5405	I5602	HVDC TRANSMISSION	30	34	1
17NR2D5406	I4301	ELECTRICAL MACHINE MODELING & ANALYSIS	35	15	0
17NR2D5406	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	35	2	0
17NR2D5406	I4303	POWER ELECTRONIC CONTROL OF DC DRIVES	30	24	1
17NR2D5406	I4304	FLEXIBLE AC TRANSMISSION SYSTEMS	30	24	1
17NR2D5406	I4305	POWER QUALITY	32	11	0
17NR2D5406	I4309	SIMULATION LAB	38	45	1
17NR2D5406	I5602	HVDC TRANSMISSION	33	24	1
17NR2D5701	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	34	42	1
17NR2D5701	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	31	45	1
17NR2D5701	I5704	FRONT END VLSI DESIGN LABORATORY	37	52	1
17NR2D5701	I6801	DIGITAL SYSTEM DESIGN	35	25	1
17NR2D5701	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	32	44	1
17NR2D5701	I6803	CMOS ANALOG IC DESIGN	31	46	1
17NR2D5701	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	32	53	1
17NR2D5702	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	32	31	1
17NR2D5702	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	37	41	1
17NR2D5702	I5704	FRONT END VLSI DESIGN LABORATORY	34	54	1
17NR2D5702	I6801	DIGITAL SYSTEM DESIGN	37	24	1
17NR2D5702	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	37	44	1
17NR2D5702	I6803	CMOS ANALOG IC DESIGN	38	43	1
17NR2D5702	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	33	34	1
17NR2D5703	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	37	29	1
17NR2D5703	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	35	38	1
17NR2D5703	I5704	FRONT END VLSI DESIGN LABORATORY	32	57	1
17NR2D5703	I6801	DIGITAL SYSTEM DESIGN	35	24	1
17NR2D5703	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	33	35	1
17NR2D5703	I6803	CMOS ANALOG IC DESIGN	36	34	1
17NR2D5703	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	28	27	1
17NR2D5704	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	35	11	0
17NR2D5704	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	36	27	1
17NR2D5704	I5704	FRONT END VLSI DESIGN LABORATORY	33	53	1
17NR2D5704	I6801	DIGITAL SYSTEM DESIGN	36	12	0
17NR2D5704	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	34	27	1
17NR2D5704	I6803	CMOS ANALOG IC DESIGN	32	37	1
17NR2D5704	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	30	26	1
17NR2D5705	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	32	14	0
17NR2D5705	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	38	28	1
17NR2D5705	I5704	FRONT END VLSI DESIGN LABORATORY	38	55	1
17NR2D5705	I6801	DIGITAL SYSTEM DESIGN	38	12	0
17NR2D5705	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	34	30	1
17NR2D5705	I6803	CMOS ANALOG IC DESIGN	36	29	1
17NR2D5705	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	33	30	1
17NR2D5706	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	36	25	1
17NR2D5706	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	33	34	1
17NR2D5706	I5704	FRONT END VLSI DESIGN LABORATORY	34	54	1
17NR2D5706	I6801	DIGITAL SYSTEM DESIGN	37	17	0
17NR2D5706	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	37	35	1
17NR2D5706	I6803	CMOS ANALOG IC DESIGN	35	46	1
17NR2D5706	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	33	38	1
17NR2D5707	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	31	26	1

Htno	Subcode	Subname	Internal	External	credits
17NR2D5707	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	37	40	1
17NR2D5707	I5704	FRONT END VLSI DESIGN LABORATORY	35	55	1
17NR2D5707	I6801	DIGITAL SYSTEM DESIGN	37	24	1
17NR2D5707	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	34	43	1
17NR2D5707	I6803	CMOS ANALOG IC DESIGN	37	34	1
17NR2D5707	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	34	39	1
17NR2D5708	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	33	30	1
17NR2D5708	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	36	39	1
17NR2D5708	I5704	FRONT END VLSI DESIGN LABORATORY	34	53	1
17NR2D5708	I6801	DIGITAL SYSTEM DESIGN	37	24	1
17NR2D5708	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	37	41	1
17NR2D5708	I6803	CMOS ANALOG IC DESIGN	37	45	1
17NR2D5708	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	34	43	1
17NR2D5709	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	31	49	1
17NR2D5709	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	34	46	1
17NR2D5709	I5704	FRONT END VLSI DESIGN LABORATORY	36	54	1
17NR2D5709	I6801	DIGITAL SYSTEM DESIGN	38	28	1
17NR2D5709	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	33	51	1
17NR2D5709	I6803	CMOS ANALOG IC DESIGN	34	54	1
17NR2D5709	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	33	39	1
17NR2D5710	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	32	33	1
17NR2D5710	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	37	47	1
17NR2D5710	I5704	FRONT END VLSI DESIGN LABORATORY	36	53	1
17NR2D5710	I6801	DIGITAL SYSTEM DESIGN	37	24	1
17NR2D5710	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	37	45	1
17NR2D5710	I6803	CMOS ANALOG IC DESIGN	36	45	1
17NR2D5710	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	34	32	1
17NR2D5711	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	37	33	1
17NR2D5711	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	37	34	1
17NR2D5711	I5704	FRONT END VLSI DESIGN LABORATORY	33	55	1
17NR2D5711	I6801	DIGITAL SYSTEM DESIGN	36	14	0
17NR2D5711	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	37	46	1
17NR2D5711	I6803	CMOS ANALOG IC DESIGN	35	42	1
17NR2D5711	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	30	38	1
17NR2D5712	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	35	28	1
17NR2D5712	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	34	43	1
17NR2D5712	I5704	FRONT END VLSI DESIGN LABORATORY	36	55	1
17NR2D5712	I6801	DIGITAL SYSTEM DESIGN	35	24	1
17NR2D5712	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	35	39	1
17NR2D5712	I6803	CMOS ANALOG IC DESIGN	35	45	1
17NR2D5712	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	32	46	1
17NR2D5713	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	38	25	1
17NR2D5713	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	37	36	1
17NR2D5713	I5704	FRONT END VLSI DESIGN LABORATORY	35	55	1
17NR2D5713	I6801	DIGITAL SYSTEM DESIGN	38	15	0
17NR2D5713	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	38	31	1
17NR2D5713	I6803	CMOS ANALOG IC DESIGN	33	43	1
17NR2D5713	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	31	39	1
17NR2D5714	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	30	24	1
17NR2D5714	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	37	26	1
17NR2D5714	I5704	FRONT END VLSI DESIGN LABORATORY	36	56	1

Htno	Subcode	Subname	Internal	External	credits
17NR2D5714	I6801	DIGITAL SYSTEM DESIGN	35	10	0
17NR2D5714	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	31	27	1
17NR2D5714	I6803	CMOS ANALOG IC DESIGN	30	44	1
17NR2D5714	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	23	28	1
17NR2D5715	I5701	DIGITAL DESIGN USING HDL ELECTIVE 1	28	11	0
17NR2D5715	I5702	CPLD AND FPGA ARCHITECTURES AND APPLICAT	33	25	1
17NR2D5715	I5704	FRONT END VLSI DESIGN LABORATORY	33	54	1
17NR2D5715	I6801	DIGITAL SYSTEM DESIGN	33	4	0
17NR2D5715	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	29	28	1
17NR2D5715	I6803	CMOS ANALOG IC DESIGN	32	21	0
17NR2D5715	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	24	30	1
17NR2D5901	I2504	WEB TECHNOLOGIES	36	36	1
17NR2D5901	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	35	39	1
17NR2D5901	I4003	PARALLEL ALGORITHMS	36	30	1
17NR2D5901	I4004	DATA MINING AND KNOWLEDGE DISCOVERY	34	24	1
17NR2D5901	I5901	DISTRIBUTED SYSTEMS	34	28	1
17NR2D5901	I5902	CST LAB 1	36	54	1
17NR2D5901	I8401	CYBER SECURITY	36	30	1
17NR2D5902	I2504	WEB TECHNOLOGIES	36	29	1
17NR2D5902	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	36	36	1
17NR2D5902	I4003	PARALLEL ALGORITHMS	35	36	1
17NR2D5902	I4004	DATA MINING AND KNOWLEDGE DISCOVERY	35	26	1
17NR2D5902	I5901	DISTRIBUTED SYSTEMS	36	34	1
17NR2D5902	I5902	CST LAB 1	38	56	1
17NR2D5902	I8401	CYBER SECURITY	37	31	1
17NR2D5903	I2504	WEB TECHNOLOGIES	36	36	1
17NR2D5903	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	35	42	1
17NR2D5903	I4003	PARALLEL ALGORITHMS	35	43	1
17NR2D5903	I4004	DATA MINING AND KNOWLEDGE DISCOVERY	36	34	1
17NR2D5903	I5901	DISTRIBUTED SYSTEMS	35	42	1
17NR2D5903	I5902	CST LAB 1	36	54	1
17NR2D5903	I8401	CYBER SECURITY	37	36	1
17NR2D5904	I2504	WEB TECHNOLOGIES	36	32	1
17NR2D5904	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	36	39	1
17NR2D5904	I4003	PARALLEL ALGORITHMS	37	12	0
17NR2D5904	I4004	DATA MINING AND KNOWLEDGE DISCOVERY	35	25	1
17NR2D5904	I5901	DISTRIBUTED SYSTEMS	37	24	1
17NR2D5904	I5902	CST LAB 1	36	54	1
17NR2D5904	I8401	CYBER SECURITY	36	28	1
17NR2D5905	I2504	WEB TECHNOLOGIES	36	32	1
17NR2D5905	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	36	40	1
17NR2D5905	I4003	PARALLEL ALGORITHMS	35	28	1
17NR2D5905	I4004	DATA MINING AND KNOWLEDGE DISCOVERY	34	30	1
17NR2D5905	I5901	DISTRIBUTED SYSTEMS	35	38	1
17NR2D5905	I5902	CST LAB 1	36	54	1
17NR2D5905	I8401	CYBER SECURITY	36	29	1
17NR2D5906	I2504	WEB TECHNOLOGIES	36	33	1
17NR2D5906	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	35	41	1
17NR2D5906	I4003	PARALLEL ALGORITHMS	37	35	1
17NR2D5906	I4004	DATA MINING AND KNOWLEDGE DISCOVERY	36	31	1
17NR2D5906	I5901	DISTRIBUTED SYSTEMS	37	33	1

Htno	Subcode	Subname	Internal	External	credits
17NR2D5906	I5902	CST LAB 1	38	55	1
17NR2D5906	I8401	CYBER SECURITY	35	29	1
17NR2D5907	I2504	WEB TECHNOLOGIES	34	29	1
17NR2D5907	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	36	41	1
17NR2D5907	I4003	PARALLEL ALGORITHMS	36	27	1
17NR2D5907	I4004	DATA MINING AND KNOWLEDGE DISCOVERY	35	30	1
17NR2D5907	I5901	DISTRIBUTED SYSTEMS	37	33	1
17NR2D5907	I5902	CST LAB 1	37	56	1
17NR2D5907	I8401	CYBER SECURITY	35	28	1
17NR2D5908	I2504	WEB TECHNOLOGIES	35	38	1
17NR2D5908	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	37	40	1
17NR2D5908	I4003	PARALLEL ALGORITHMS	36	36	1
17NR2D5908	I4004	DATA MINING AND KNOWLEDGE DISCOVERY	37	33	1
17NR2D5908	I5901	DISTRIBUTED SYSTEMS	36	31	1
17NR2D5908	I5902	CST LAB 1	39	58	1
17NR2D5908	I8401	CYBER SECURITY	37	31	1
17NR2D5909	I2504	WEB TECHNOLOGIES	37	36	1
17NR2D5909	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	35	34	1
17NR2D5909	I4003	PARALLEL ALGORITHMS	37	36	1
17NR2D5909	I4004	DATA MINING AND KNOWLEDGE DISCOVERY	36	27	1
17NR2D5909	I5901	DISTRIBUTED SYSTEMS	36	44	1
17NR2D5909	I5902	CST LAB 1	37	55	1
17NR2D5909	I8401	CYBER SECURITY	35	30	1
17NR2D5910	I2504	WEB TECHNOLOGIES	37	-1	0
17NR2D5910	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	35	30	1
17NR2D5910	I4003	PARALLEL ALGORITHMS	36	-1	0
17NR2D5910	I4004	DATA MINING AND KNOWLEDGE DISCOVERY	37	24	1
17NR2D5910	I5901	DISTRIBUTED SYSTEMS	37	14	0
17NR2D5910	I5902	CST LAB 1	36	56	1
17NR2D5910	I8401	CYBER SECURITY	34	18	0
17NR2D5911	I2504	WEB TECHNOLOGIES	36	33	1
17NR2D5911	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	35	36	1
17NR2D5911	I4003	PARALLEL ALGORITHMS	36	30	1
17NR2D5911	I4004	DATA MINING AND KNOWLEDGE DISCOVERY	36	31	1
17NR2D5911	I5901	DISTRIBUTED SYSTEMS	36	35	1
17NR2D5911	I5902	CST LAB 1	36	54	1
17NR2D5911	I8401	CYBER SECURITY	36	33	1
17NR2D5912	I2504	WEB TECHNOLOGIES	18	-1	0
17NR2D5912	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	17	-1	0
17NR2D5912	I4003	PARALLEL ALGORITHMS	17	-1	0
17NR2D5912	I4004	DATA MINING AND KNOWLEDGE DISCOVERY	17	-1	0
17NR2D5912	I5901	DISTRIBUTED SYSTEMS	17	-1	0
17NR2D5912	I5902	CST LAB 1	0	-1	0
17NR2D5912	I8401	CYBER SECURITY	17	-1	0
17NR2D8701	I2201	ADVANCED MATHEMATICS	33	44	1
17NR2D8701	I8701	THEORY OF ELASTICITY	35	24	1
17NR2D8701	I8702	MATRIX ANALYSIS OF STRUCTURES	36	35	1
17NR2D8701	I8703	STRUCTURAL DYNAMICS	34	28	1
17NR2D8701	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	29	26	1
17NR2D8701	I8707	REPAIR AND REHABILITATION OF STRUCTURES	34	42	1
17NR2D8701	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	35	55	1

Htno	Subcode	Subname	Internal	External	credits
17NR2D8702	I2201	ADVANCED MATHEMATICS	33	10	0
17NR2D8702	I8701	THEORY OF ELASTICITY	37	17	0
17NR2D8702	I8702	MATRIX ANALYSIS OF STRUCTURES	35	36	1
17NR2D8702	I8703	STRUCTURAL DYNAMICS	35	5	0
17NR2D8702	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	33	10	0
17NR2D8702	I8707	REPAIR AND REHABILITATION OF STRUCTURES	35	31	1
17NR2D8702	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	31	44	1
17NR2D8703	I2201	ADVANCED MATHEMATICS	30	-1	0
17NR2D8703	I8701	THEORY OF ELASTICITY	30	-1	0
17NR2D8703	I8702	MATRIX ANALYSIS OF STRUCTURES	31	-1	0
17NR2D8703	I8703	STRUCTURAL DYNAMICS	32	-1	0
17NR2D8703	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	31	-1	0
17NR2D8703	I8707	REPAIR AND REHABILITATION OF STRUCTURES	31	-1	0
17NR2D8703	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	30	-1	0
17NR2D8704	I2201	ADVANCED MATHEMATICS	34	28	1
17NR2D8704	I8701	THEORY OF ELASTICITY	31	24	1
17NR2D8704	I8702	MATRIX ANALYSIS OF STRUCTURES	32	38	1
17NR2D8704	I8703	STRUCTURAL DYNAMICS	34	28	1
17NR2D8704	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	31	24	1
17NR2D8704	I8707	REPAIR AND REHABILITATION OF STRUCTURES	32	35	1
17NR2D8704	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	31	44	1
17NR2D8705	I2201	ADVANCED MATHEMATICS	32	5	0
17NR2D8705	I8701	THEORY OF ELASTICITY	32	7	0
17NR2D8705	I8702	MATRIX ANALYSIS OF STRUCTURES	32	37	1
17NR2D8705	I8703	STRUCTURAL DYNAMICS	34	3	0
17NR2D8705	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	30	12	0
17NR2D8705	I8707	REPAIR AND REHABILITATION OF STRUCTURES	33	30	1
17NR2D8705	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	31	44	1
17NR2D8706	I2201	ADVANCED MATHEMATICS	33	15	0
17NR2D8706	I8701	THEORY OF ELASTICITY	36	14	0
17NR2D8706	I8702	MATRIX ANALYSIS OF STRUCTURES	36	37	1
17NR2D8706	I8703	STRUCTURAL DYNAMICS	35	2	0
17NR2D8706	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	34	12	0
17NR2D8706	I8707	REPAIR AND REHABILITATION OF STRUCTURES	33	36	1
17NR2D8706	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	32	47	1
17NR2D8707	I2201	ADVANCED MATHEMATICS	34	13	0
17NR2D8707	I8701	THEORY OF ELASTICITY	34	24	1
17NR2D8707	I8702	MATRIX ANALYSIS OF STRUCTURES	33	45	1
17NR2D8707	I8703	STRUCTURAL DYNAMICS	34	10	0
17NR2D8707	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	30	9	0
17NR2D8707	I8707	REPAIR AND REHABILITATION OF STRUCTURES	34	32	1
17NR2D8707	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	32	47	1
17NR2D8708	I2201	ADVANCED MATHEMATICS	36	24	1
17NR2D8708	I8701	THEORY OF ELASTICITY	36	24	1
17NR2D8708	I8702	MATRIX ANALYSIS OF STRUCTURES	35	40	1
17NR2D8708	I8703	STRUCTURAL DYNAMICS	35	10	0
17NR2D8708	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	33	24	1
17NR2D8708	I8707	REPAIR AND REHABILITATION OF STRUCTURES	35	37	1
17NR2D8708	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	33	50	1
17NR2D8709	I2201	ADVANCED MATHEMATICS	35	24	1
17NR2D8709	I8701	THEORY OF ELASTICITY	35	24	1

Htno	Subcode	Subname	Internal	External	credits
17NR2D8709	I8702	MATRIX ANALYSIS OF STRUCTURES	35	30	1
17NR2D8709	I8703	STRUCTURAL DYNAMICS	36	10	0
17NR2D8709	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	35	24	1
17NR2D8709	I8707	REPAIR AND REHABILITATION OF STRUCTURES	35	37	1
17NR2D8709	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	31	44	1
17NR2D8710	I2201	ADVANCED MATHEMATICS	31	24	1
17NR2D8710	I8701	THEORY OF ELASTICITY	33	26	1
17NR2D8710	I8702	MATRIX ANALYSIS OF STRUCTURES	32	37	1
17NR2D8710	I8703	STRUCTURAL DYNAMICS	33	24	1
17NR2D8710	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	27	24	1
17NR2D8710	I8707	REPAIR AND REHABILITATION OF STRUCTURES	32	34	1
17NR2D8710	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	30	40	1
17NR2D8711	I2201	ADVANCED MATHEMATICS	34	18	0
17NR2D8711	I8701	THEORY OF ELASTICITY	35	-1	0
17NR2D8711	I8702	MATRIX ANALYSIS OF STRUCTURES	34	33	1
17NR2D8711	I8703	STRUCTURAL DYNAMICS	25	0	0
17NR2D8711	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	33	-1	0
17NR2D8711	I8707	REPAIR AND REHABILITATION OF STRUCTURES	33	-1	0
17NR2D8711	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	33	50	1
17NR2D8712	I2201	ADVANCED MATHEMATICS	32	-1	0
17NR2D8712	I8701	THEORY OF ELASTICITY	32	-1	0
17NR2D8712	I8702	MATRIX ANALYSIS OF STRUCTURES	31	-1	0
17NR2D8712	I8703	STRUCTURAL DYNAMICS	31	-1	0
17NR2D8712	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	32	-1	0
17NR2D8712	I8707	REPAIR AND REHABILITATION OF STRUCTURES	32	-1	0
17NR2D8712	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	30	-1	0
17NR2D8713	I2201	ADVANCED MATHEMATICS	32	-1	0
17NR2D8713	I8701	THEORY OF ELASTICITY	32	-1	0
17NR2D8713	I8702	MATRIX ANALYSIS OF STRUCTURES	30	-1	0
17NR2D8713	I8703	STRUCTURAL DYNAMICS	31	-1	0
17NR2D8713	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	32	-1	0
17NR2D8713	I8707	REPAIR AND REHABILITATION OF STRUCTURES	30	-1	0
17NR2D8713	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	30	-1	0
17NR2D8714	I2201	ADVANCED MATHEMATICS	33	24	1
17NR2D8714	I8701	THEORY OF ELASTICITY	33	31	1
17NR2D8714	I8702	MATRIX ANALYSIS OF STRUCTURES	32	37	1
17NR2D8714	I8703	STRUCTURAL DYNAMICS	33	44	1
17NR2D8714	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	30	26	1
17NR2D8714	I8707	REPAIR AND REHABILITATION OF STRUCTURES	33	37	1
17NR2D8714	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	31	44	1
17NR2D8715	I2201	ADVANCED MATHEMATICS	38	2	0
17NR2D8715	I8701	THEORY OF ELASTICITY	36	3	0
17NR2D8715	I8702	MATRIX ANALYSIS OF STRUCTURES	37	28	1
17NR2D8715	I8703	STRUCTURAL DYNAMICS	36	0	0
17NR2D8715	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	37	6	0
17NR2D8715	I8707	REPAIR AND REHABILITATION OF STRUCTURES	34	34	1
17NR2D8715	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	33	50	1
17NR2D8716	I2201	ADVANCED MATHEMATICS	32	6	0
17NR2D8716	I8701	THEORY OF ELASTICITY	35	24	1
17NR2D8716	I8702	MATRIX ANALYSIS OF STRUCTURES	37	35	1
17NR2D8716	I8703	STRUCTURAL DYNAMICS	34	5	0

Htno	Subcode	Subname	Internal	External	credits
17NR2D8716	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	35	8	0
17NR2D8716	I8707	REPAIR AND REHABILITATION OF STRUCTURES	36	42	1
17NR2D8716	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	30	38	1
17NR2D8717	I2201	ADVANCED MATHEMATICS	37	31	1
17NR2D8717	I8701	THEORY OF ELASTICITY	37	16	0
17NR2D8717	I8702	MATRIX ANALYSIS OF STRUCTURES	37	32	1
17NR2D8717	I8703	STRUCTURAL DYNAMICS	34	50	1
17NR2D8717	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	36	24	1
17NR2D8717	I8707	REPAIR AND REHABILITATION OF STRUCTURES	34	36	1
17NR2D8717	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	35	55	1
17NR2D8718	I2201	ADVANCED MATHEMATICS	37	24	1
17NR2D8718	I8701	THEORY OF ELASTICITY	36	2	0
17NR2D8718	I8702	MATRIX ANALYSIS OF STRUCTURES	35	25	1
17NR2D8718	I8703	STRUCTURAL DYNAMICS	34	11	0
17NR2D8718	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	35	24	1
17NR2D8718	I8707	REPAIR AND REHABILITATION OF STRUCTURES	33	38	1
17NR2D8718	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	32	47	1

****Note:1)**For Recounting/Revaluation/Challenge By Revaluation Apply through Online(www.jntukresults.edu.in)

****NOTE:2** [Last Date for Apply Recounting/Revaluation/Challenge By Revaluation: **09-05-2018**]

****NOTE:3** [Please inform to the students to enter these subject codes for applying Recounting/Revaluation/Challenge By Revaluation]

****NOTE:**

-1 in the filed of externals indicates student absent for the respective subject.

-2 in the filed of externals indicates student result is withheld for the respective subject.

-3 in the filed of externals indicates Malpractice for the respective subject.]

Date:26-04-2018

N. Mohan Rao
Controller of Examinations